SEARCH REQUEST FORM   Scientific and Technical Information Center - EIC2800

Rev. 3/19/2007   This is an experimental format -- Please give suggestions or comments to Jeff Harrison. JEF-4B68. 22511.

Date 5/15/07 Serial # 10/798,439 Priority Application Filing Date 12/5/03

Your Name M. Lewis   Examiner # 73172

AU 2822 Phone 272-1938 Room 5A39

In what format would you like your results? Paper is the default.   (PAPER)   DISK   EMAIL

If submitting more than one search request form, please prioritize the searches in order of need.

Need before 5/21

Where have you searched so far on this case?
Circle: USPAT   USPGPUB   DWPI   EPO Abs   JPO Abs   IBM TDB
Other: _____

What relevant art have you found so far? Please attach citations or Information Disclosure Statements.
_____

What types of references would you like? Please checkmark:

Primary Refs ___✓___   Nonpatent Literature _____   Teaching Refs _____   MAY 16
Secondary Refs ___   Foreign Patents _____   Other _____

If this is a "Fast & Focused Search" request, put a checkmark here: ☐
And then please speak in-person immediately with the
EIC2800 Technical Information Specialist or with an EIC2800 Searcher.
A "Fast & Focused Search" is completed in 2 hours (maximum). The search must be on a very specific topic and meet certain criteria. The criteria are posted in EIC2800 and on the STIC NPL Web Page at
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In your own words, what is the topic, such as the novelty, motivation, utility, or other specific facets defining the desired focus of this search?   Please include the concepts, synonyms, keywords, acronyms, registry numbers, definitions, structures, strategies, and anything else that helps to describe the topic. Please attach a copy of the abstract and pertinent claims.

Claims 1-8, 10-13, 15-18
CMOS / Memory
Decrease pitch to provide
more substrate area

Problem, See paragraphs 2-4
solution. " abstract.

5,060,043   6,486,066
6,631,085   2003/0198101

Staff Use Only	Type of Search	Vendors	
Searcher: Harrison	Structure (#) _____	STN ___X___	
Searcher Phone: 22511	Bibliographic __X__	Dialog ___X___	
Searcher Location: STIC-EIC2800, JEF-4B68	Litigation _____	Questel/Orbit _____	
Date Searcher Picked Up: 5-22	Fulltext __X__	Lexis-Nexis _____	
Date Completed: 5-22-07	Patent Family _____	WWW/Internet _____	
Searcher Prep/Rev Time: 63	Other DPCI	Other _____	
Online Time: 84			

CAS/STN

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      FILE 'WPIX, HCAPLUS' ENTERED AT 14:49:40 ON 22 MAY 2007
L1          6 SEA ABB=ON  PLU=ON  (US5060045 OR US6631085 OR US6486066 OR US20030198101)/PN
L2            SEL PLU=ON  L1 1- PN :      15 TERMS


      FILE 'DPCI' ENTERED AT 14:49:58 ON 22 MAY 2007
L3         58 SEA ABB=ON  PLU=ON  L2/PN.D
L4            SEL PLU=ON  L3 1- PN :     205 TERMS


      FILE 'WPIX, JAPIO, KOREAPAT, HCAPLUS' ENTERED AT 14:50:32 ON 22 MAY 2007
L5        100 SEA ABB=ON  PLU=ON  L4
L6          5 SEA ABB=ON  PLU=ON  L5 AND (3D OR THREE D OR THREE DIMENSION#### OR
VERTICAL)(3A)(MEMORY)
L7          1 SEA ABB=ON  PLU=ON  L5 AND 3 D(3A)(MEMORY)
L8        132 SEA ABB=ON  PLU=ON  3 D(3A)(MEMORY)
L9       4221 SEA ABB=ON  PLU=ON  (3D OR THREE D OR THREE DIMENSION#### OR VERTICAL)(3A)(MEMORY)
L10       338 SEA ABB=ON  PLU=ON  MEMORY(2A) PITCH
L11         3 SEA ABB=ON  PLU=ON  MEMORY(2A) CENTER(2W) CENTER
L12         3 SEA ABB=ON  PLU=ON  MEMORY(2A) CENTER#####(2W) CENTER#####
L13      5252 SEA ABB=ON  PLU=ON  MEMORY(2A) LEVEL
L14      1163 SEA ABB=ON  PLU=ON  MEMORY(2A) UPPER
L15      1525 SEA ABB=ON  PLU=ON  MEMORY(2A) ABOVE
L16      2675 SEA ABB=ON  PLU=ON  MEMORY(2A) OVER
L17     51213 SEA ABB=ON  PLU=ON  (SUBSTRATE OR TRANSISTOR OR DEVICE OR FET OR MOS OR CMOS### OR
?MOSFET? OR SI OR SILICON OR SEMICONDUCT####)(2A) LEVEL
L18    480696 SEA ABB=ON  PLU=ON  (SUBSTRATE OR TRANSISTOR OR DEVICE OR FET OR MOS OR CMOS### OR
?MOSFET? OR SI OR SILICON OR SEMICONDUCT####)(2A) LAYER
L19      4982 SEA ABB=ON  PLU=ON  WAFER(2A) LEVEL
L20     12558 SEA ABB=ON  PLU=ON  WAFER(2A) LAYER
L21         8 SEA ABB=ON  PLU=ON  L5 AND (L9 OR L10 OR L11 OR L12 OR L13 OR L14 OR L15 OR L16)
L22         6 SEA ABB=ON  PLU=ON  L5 AND PITCH#####
L23        20 SEA ABB=ON  PLU=ON  L5 AND DENSITY
L24         0 SEA ABB=ON  PLU=ON  L5 AND DENSER
L25         4 SEA ABB=ON  PLU=ON  L5 AND (MICRON OR MU OR MUM)
L26         0 SEA ABB=ON  PLU=ON  L5 AND (SIZE#### OR AREA OR SUBSTRATE OR
                     MEMORY OR SUBSTRATE OR DIMENSION#####)(3A) CRITICAL###
L27        36 SEA ABB=ON  PLU=ON  (L6 OR L7) OR (L11 OR L12) OR (L21 OR L22
                     OR L23 OR L24 OR L25 OR L26)
L28     24484 SEA ABB=ON  PLU=ON  (SIZE#### OR AREA OR SUBSTRATE OR MEMORY
                     OR SUBSTRATE OR DIMENSION#####)(3A) CRITICAL###
L29      1337 SEA ABB=ON  PLU=ON  (L8 OR L9 OR L10 OR L11 OR L12 OR L13 OR
                     L14 OR L15 OR L16) AND (L17 OR L18 OR L19 OR L20 OR L21 OR L22
                     OR L23 OR L24 OR L25 OR L26 OR L27)
L30         6 SEA ABB=ON  PLU=ON  L28 AND L29
L31        42 SEA ABB=ON  PLU=ON  L27 OR L30
L32        31 SEA ABB=ON  PLU=ON  L31 AND 1990-2003/PY
L33        42 SEA ABB=ON  PLU=ON  L31 AND 1990-2003/PRY
L34         0 SEA ABB=ON  PLU=ON  L31 AND 1980-1989/PRY
L35         0 SEA ABB=ON  PLU=ON  L31 AND 1980-1989/PY
L36        42 SEA ABB=ON  PLU=ON  L32 OR L33
L37         4 SEA ABB=ON  PLU=ON  L36 AND LEVELS
L38         7 SEA ABB=ON  PLU=ON  L36 AND ?PITCH?
L39         1 SEA ABB=ON  PLU=ON  L36 AND MULTILEVEL?
L40         2 SEA ABB=ON  PLU=ON  L36 AND MULTI LEVEL####
L41         0 SEA ABB=ON  PLU=ON  L36 AND BILEVEL###
L42         0 SEA ABB=ON  PLU=ON  L36 AND DILEVEL###
L43         0 SEA ABB=ON  PLU=ON  L36 AND TRILEVEL###
L44         0 SEA ABB=ON  PLU=ON  L36 AND TRI LEVEL###
L45         2 SEA ABB=ON  PLU=ON  L36 AND (ANOTHER OR SECOND OR UPPER OR
                     LOWER OR TOP OR BOTTOM OR TWO OR THREE OR THIRD OR 2ND OR 3RD
                     OR 2 OR 3 DIFFERENT)(1W) LEVEL####
L46        11 SEA ABB=ON  PLU=ON  (L37 OR L38 OR L39 OR L40 OR L41 OR L42 OR
                     L43 OR L44 OR L45)


      FILE 'STNGUIDE' ENTERED AT 15:07:13 ON 22 MAY 2007


      FILE 'WPIX, JAPIO, KOREAPAT, HCAPLUS' ENTERED AT 15:09:17 ON 22 MAY 2007
L47         0 SEA ABB=ON  PLU=ON  MEMORY LEVEL AND SUBSTRATE LEVEL
L48       492 SEA ABB=ON  PLU=ON  MEMORY(11A) SUBSTRATE AND ?LEVEL? AND
                     (TRANSISTOR###### OR CMOS#######)
L49        71 SEA ABB=ON  PLU=ON  L48 AND DENSITY
L50         6 SEA ABB=ON  PLU=ON  L48 AND PITCH#####
L51         0 SEA ABB=ON  PLU=ON  L48 AND (CENTRE##### OR CENTER#####)(1W)(CE
                     NTRE##### OR CENTER#####)
L52        75 SEA ABB=ON  PLU=ON  (L49 OR L50)
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L53       2 SEA ABB=ON   PLU=ON   L49 AND L50
L54      .2 SEA ABB=ON   PLU=ON   L53 NOT L46
L55     295 SEA ABB=ON   PLU=ON   (L5 OR L6 OR L7 OR L8 OR L9 OR L10 OR L11
            OR L12 OR L13 OR L14 OR L15 OR L16 OR L17 OR L18 OR L19 OR L20
            OR L21 OR L22 OR L23 OR L24 OR L25 OR L26 OR L27 OR L28 OR L29
            OR L30 OR L31 OR L32 OR L33 OR L34 OR L35 OR L36 OR L37 OR L38
            OR L39 OR L40 OR L41 OR L42 OR L43 OR L44 OR L45 OR L46) AND RAILS
L56       2 SEA ABB=ON   PLU=ON   L48 AND RAILS
L57     125 SEA ABB=ON   PLU=ON   L48 AND LINES
L58   14655 SEA ABB=ON   PLU=ON   (L5 OR L6 OR L7 OR L8 OR L9 OR L10 OR L11
            OR L12 OR L13 OR L14 OR L15 OR L16 OR L17 OR L18 OR L19 OR L20
            OR L21 OR L22 OR L23 OR L24 OR L25 OR L26 OR L27 OR L28 OR L29
            OR L30 OR L31 OR L32 OR L33 OR L34 OR L35 OR L36 OR L37 OR L38
            OR L39 OR L40 OR L41 OR L42 OR L43 OR L44 OR L45 OR L46) AND LINES
L59     731 SEA ABB=ON   PLU=ON   (L5 OR L6 OR L7 OR L8 OR L9 OR L10 OR L11
            OR L12 OR L13 OR L14 OR L15 OR L16 OR L17 OR L18 OR L19 OR L20
            OR L21 OR L22 OR L23 OR L24 OR L25 OR L26 OR L27 OR L28 OR L29
            OR L30 OR L31 OR L32 OR L33 OR L34 OR L35 OR L36 OR L37 OR L38
            OR L39 OR L40 OR L41 OR L42 OR L43 OR L44 OR L45 OR L46) AND
            MEMORY(1A)(LINE OR RAIL)
L60    4006 SEA ABB=ON   PLU=ON   L58 AND MEMORY
L61    1376 SEA ABB=ON   PLU=ON   L60 AND (TRANSISTOR OR CMOS###### OR
            COMPLEMENTARY MOS####)
L62    5123 SEA ABB=ON   PLU=ON   (L48 OR L49 OR L50 OR L51 OR L52 OR L53 OR
            L54 OR L55 OR L56 OR L57) OR L59 OR L60
L63    2746 SEA ABB=ON   PLU=ON   (L48 OR L49 OR L50 OR L51 OR L52 OR L53 OR
            L54 OR L55 OR L56 OR L57) OR L59 OR L61
L64       2 SEA ABB=ON   PLU=ON   L63 AND DENS#####(4A) PITCH#####
L65      12 SEA ABB=ON   PLU=ON   L63 AND DENS#####(4A) SUBSTRATE
L66     151 SEA ABB=ON   PLU=ON   L63 AND DENS#####(4A) MEMORY
L67      13 SEA ABB=ON   PLU=ON   L63 AND DENS#####(4A) LEVEL
L68      16 SEA ABB=ON   PLU=ON   L63 AND DENS#####(4A)(TRANSISTOR OR CMOS#####)
L69       5 SEA ABB=ON   PLU=ON   L63 AND PITCH######(4A)(TRANSISTOR OR CMOS#####)
L70       3 SEA ABB=ON   PLU=ON   L63 AND PITCH####(4A) LEVEL
L71      29 SEA ABB=ON   PLU=ON   L63 AND PITCH#####(4A) MEMORY
L72      13 SEA ABB=ON   PLU=ON   L46 OR L54
L73     210 SEA ABB=ON   PLU=ON   (L65 OR L66 OR L67 OR L68 OR L69 OR L70 OR L71 OR L72)
L74     204 SEA ABB=ON   PLU=ON   (L57 OR L58 OR L59 OR L60 OR L61 OR L62 OR L63) AND L73
L75       0 SEA ABB=ON   PLU=ON   L56 NOT (L54 OR L46)
L76      81 SEA ABB=ON   PLU=ON   (L64 OR L65) OR (L67 OR L68 OR L69 OR L70 OR L71 OR L72)
L77     211 SEA ABB=ON   PLU=ON   L73 OR L76
L78     198 SEA ABB=ON   PLU=ON   L77 NOT (L53 OR L54 OR L46)
L79       4 SEA ABB=ON   PLU=ON   L78 AND CRITICAL
L80       5 SEA ABB=ON   PLU=ON   L78 AND (HIGHER OR LOWER OR GREATER OR
            LESS OR MORE OR COMPAR##### OR DIFFER#######)(3A) PITCH#####
L81      20 SEA ABB=ON   PLU=ON   L78 AND (HIGHER OR LOWER OR GREATER OR
            LESS OR MORE OR COMPAR##### OR DIFFER#######)(3A) DENS######
L82       2 SEA ABB=ON   PLU=ON   L78 AND (HIGHER OR LOWER OR GREATER OR
            LESS OR MORE OR COMPAR##### OR DIFFER#######)(3A) GAP
L83       8 SEA ABB=ON   PLU=ON   L78 AND (HIGHER OR LOWER OR GREATER OR
            LESS OR MORE OR COMPAR##### OR DIFFER#######)(3A) BETWEEN
L84       2 SEA ABB=ON   PLU=ON   L78 AND (HIGHER OR LOWER OR GREATER OR
            LESS OR MORE OR COMPAR##### OR DIFFER#######)(3A) SPAC####
L85       5 SEA ABB=ON   PLU=ON   L78 AND (HIGHER OR LOWER OR GREATER OR
            LESS OR MORE OR COMPAR##### OR DIFFER#######)(3A) AREA
L86     151 SEA ABB=ON   PLU=ON   L66 AND (L65 OR (L66 OR L67 OR L68 OR L69
            OR L70 OR L71 OR L72 OR L73 OR L74 OR L75 OR L76 OR L77 OR L78
            OR L79 OR L80 OR L81))
L87     198 SEA ABB=ON   PLU=ON   (L64 OR L65 OR L66 OR L67 OR L68 OR L69 OR
            L70 OR L71 OR L72 OR L73 OR L74 OR L75 OR L76 OR L77 OR L78 OR
            L79 OR L80 OR L81 OR L82 OR L83 OR L84 OR L85 OR L86) NOT (L54 OR L46)
L88      90 SEA ABB=ON   PLU=ON   L87 AND ((L64 OR L65) OR (L67 OR L68 OR
            L69 OR L70 OR L71 OR L72) OR (L79 OR L80 OR L81 OR L82 OR L83
            OR L84 OR L85))
L89       1 SEA ABB=ON   PLU=ON   L88 AND RAILS
L90      65 SEA ABB=ON   PLU=ON   L88 AND LINES
L91      33 SEA ABB=ON   PLU=ON   L88 AND ?LEVEL?
L92      84 SEA ABB=ON   PLU=ON   (L79 OR L80 OR L81 OR L82 OR L83 OR L84 OR
            L85) OR (L89 OR L90 OR L91)
L93      84 SEA ABB=ON   PLU=ON   L92 NOT (L54 OR L46)
L94      33 SEA ABB=ON   PLU=ON   L93 AND AREA
L95      63 SEA ABB=ON   PLU=ON   L93 AND TRANSISTOR
L96       7 SEA ABB=ON   PLU=ON   L93 AND CMOS######
L97      83 SEA ABB=ON   PLU=ON   L93 AND MEMORY
L98      58 SEA ABB=ON   PLU=ON   L93 AND CELL
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L99       54 SEA ABB=ON   PLU=ON   L93 AND SUBSTRATE
L100      63 SEA ABB=ON   PLU=ON   L95 AND L97
L101      50 SEA ABB=ON   PLU=ON   L95 AND L97 AND L98
L102      36 SEA ABB=ON   PLU=ON   L95 AND L97 AND L98 AND L99
L103      12 SEA ABB=ON   PLU=ON   L93 AND AREAS
L104       8 SEA ABB=ON   PLU=ON   L93 AND AREA(10A) AREA
L105      65 SEA ABB=ON   PLU=ON   L89 OR L91 OR L94 OR L96 OR L102 OR L104
L106      11 SEA ABB=ON   PLU=ON   L105 AND (SUBSTRATE OR TRANSISTOR OR
                                   CMOS##### OR MEMORY)(4A) ?LEVEL?
L107      11 SEA ABB=ON   PLU=ON   L106 NOT (L54 OR L46)
L108      42 SEA ABB=ON   PLU=ON   L27 OR (L30 OR L31 OR L32 OR L33 OR L34 OR
                                   L35 OR L36 OR L37 OR L38 OR L39 OR L40 OR L41 OR L42 OR L43 OR
                                   L44 OR L45)
L109   15769 SEA ABB=ON   PLU=ON   (L47 OR L48 OR L49 OR L50 OR L51 OR L52 OR
                                   L53 OR L54 OR L55 OR L56 OR L57 OR L58 OR L59 OR L60 OR L61 OR
                                   L62 OR L63 OR L64 OR L65 OR L66 OR L67 OR L68 OR L69 OR L70 OR
                                   L71 OR L72 OR L73 OR L74 OR L75 OR L76 OR L77 OR L78 OR L79 OR L80)
L110     199 SEA ABB=ON   PLU=ON   (L81 OR L82 OR L83 OR L84 OR L85 OR L86 OR
                                   L87 OR L88 OR L89 OR L90 OR L91 OR L92 OR L93 OR L94 OR L95 OR
                                   L96 OR L97 OR L98 OR L99 OR L100 OR L101 OR L102 OR L103 OR
                                   L104 OR L105 OR L106 OR L107)
L111      24 SEA ABB=ON   PLU=ON   L107 OR L54 OR L46
L112   15763 SEA ABB=ON   PLU=ON   (L108 OR L109 OR L110) NOT L111
L113    1249 SEA ABB=ON   PLU=ON   L112 AND MEMORY(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L114     951 SEA ABB=ON   PLU=ON   L112 AND SUBSTRATE(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L115     688 SEA ABB=ON   PLU=ON   L112 AND TRANSISTOR(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L116      78 SEA ABB=ON   PLU=ON   L112 AND CMOS#####(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L117     115 SEA ABB=ON   PLU=ON   L112 AND MOS(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L118      73 SEA ABB=ON   PLU=ON   L112 AND ?MOSFET?(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L119      59 SEA ABB=ON   PLU=ON   L112 AND FET(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L120    1239 SEA ABB=ON   PLU=ON   L112 AND DEVICE(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L121      42 SEA ABB=ON   PLU=ON   L112 AND ?SFET(5A)(AREA OR ?LEVEL? OR
                                   PITCH#### OR DENS#####)
L122     459 SEA ABB=ON   PLU=ON   L113 AND (L114 OR L115 OR L116 OR L117 OR
                                   L118 OR L119 OR L120 OR L121)
L123     459 SEA ABB=ON   PLU=ON   L122 NOT L111
L124      93 SEA ABB=ON   PLU=ON   L123 AND AREAS
L125      72 SEA ABB=ON   PLU=ON   L123 AND AREA(6A) AREA
L126       1 SEA ABB=ON   PLU=ON   L123 AND MEMORY(3A) RAIL
L127     129 SEA ABB=ON   PLU=ON   L123 AND MEMORY(3A) LINE
L128      44 SEA ABB=ON   PLU=ON   L124 AND L125
L129      32 SEA ABB=ON   PLU=ON   (L124 OR L125) AND L127
L130      65 SEA ABB=ON   PLU=ON   L126 OR (L128 OR L129)
L131       0 SEA ABB=ON   PLU=ON   L130 AND MEMORY(5A) PITCH#####
L132      51 SEA ABB=ON   PLU=ON   L130 AND MEMORY(5A) AREA
L133      46 SEA ABB=ON   PLU=ON   L130 AND SUBSTRATE(5A) AREA
L134      37 SEA ABB=ON   PLU=ON   L132 AND L133
L135      37 SEA ABB=ON   PLU=ON   L134 NOT L111
L136       8 SEA ABB=ON   PLU=ON   L135 AND ?LEVEL?
L137     948 SEA ABB=ON   PLU=ON   MEMORY(3A) AREA AND (SUBSTRATE OR CMOS#####
                                   OR TRANSISTOR)(3A) AREA
L138      18 SEA ABB=ON   PLU=ON   L137 AND (SUBSTRATE OR CMOS##### OR
                                   TRANSISTOR)(6A) ?LEVEL?
L139      20 SEA ABB=ON   PLU=ON   L137 AND MEMORY(6A) ?LEVEL?
L140      28 SEA ABB=ON   PLU=ON   (L138 OR L139)
L141      32 SEA ABB=ON   PLU=ON   L136 OR L111
L142      23 SEA ABB=ON   PLU=ON   L140 NOT L141
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FILE 'HCAPLUS, JAPIO, WPIX, INSPEC' ENTERED AT 17:10:05 ON 22 MAY 2007
L144   1482 SEA ABB=ON   PLU=ON   (MONOCRYST? OR SINGLE CRYST####### OR
               MONOCRYST#######)(2W)(DIE OR DICE OR WAFER OR SI OR SILICON OR
               SEMICOND######)(5A)(TRANSISTOR OR CMOS##### OR MOS OR ?MOSFET?)

FILE 'HCAPLUS, JAPIO, WPIX, INSPEC' ENTERED AT 17:10:46 ON 22 MAY 2007
L145   423 SEA ABB=ON   PLU=ON   (MONOCRYST? OR SINGLE CRYST####### OR
               MONOCRYST#######)(2W) SUBSTRATE(5A)(TRANSISTOR OR CMOS##### OR
               MOS OR ?MOSFET?)
L146   246 SEA ABB=ON   PLU=ON   L144 AND L145
L147   3 SEA ABB=ON   PLU=ON   L146 AND ?LEVEL?(4A)(MEMORY OR TRANSISTOR
               OR SUBSTRATE OR DEVICE OR CMOS#######)
             D TI 1-3
             D MAX 1-3
L148   822 SEA ABB=ON   PLU=ON   (MONO CRYST? OR SINGLE CRYST####### OR
               MONOCRYST#######)(5A) SUBSTRATE(5A)(TRANSISTORS OR CMOSFETS OR
               DEVICES OR ?FETS)
L149   500 SEA ABB=ON   PLU=ON   (MONO CRYST? OR SINGLE CRYST####### OR
               MONOCRYST#######)(2W) SUBSTRATE(5A)(TRANSISTORS OR CMOSFETS OR
               DEVICES OR ?FETS)
L150   72 SEA ABB=ON   PLU=ON   (MONO CRYST? OR SINGLE CRYST####### OR
               MONOCRYST#######)(2W) SUBSTRATE(5A) TRANSISTORS
L151   46 SEA ABB=ON   PLU=ON   (MONO CRYST? OR SINGLE CRYST####### OR
               MONOCRYST#######)(2W) SUBSTRATE(3A) TRANSISTORS
L152   2 SEA ABB=ON   PLU=ON   L151 AND MEMORY
             D MAX 1-2
L153   1138 SEA ABB=ON   PLU=ON   (BILEVEL? OR DILEVEL? OR MULTILEVEL? OR
               (DUAL OR BI OR DI OR TWO OR DOUBLE OR SECOND OR UPPER OR TOP
               OR MULTI OR MULTIPLE OR 2)(W) LEVEL##)(1W) MEMORY
L154   1 SEA ABB=ON   PLU=ON   L153 AND SUBSTRATE(2W)(CMOS###### OR
               TRANSISTOR)
L155   2 SEA ABB=ON   PLU=ON   L153 AND SUBSTRATE(2A)(CMOS###### OR
               TRANSISTOR)
             D MAX 1-2
L156   9 SEA ABB=ON   PLU=ON   L153 AND SUBSTRATE(5A)(CMOS###### OR
               TRANSISTOR)
L157   7 SEA ABB=ON   PLU=ON   L156 NOT L155
             D TI 1-7
             D MAX 3-6
             D ALL 1 2 7

22may07 16:21:53 User259284 Session D4041.2

File   2:INSPEC 1898-2007/May W2
       (c) 2007 Institution of Electrical Engineers

Set	Items	Description
S1	83	MULTILEVEL()MEMOR?????
S2	0	S1 AND MONOCRYST??????
S3	0	S1 AND MONO()CRYST?????????
S4	0	S1 AND MONOCRYST?????????
S5	1	S1 AND SINGLE(1W)CRYST?????????
S6	32049	R1:R2 OR R4:R5 OR R9:R10 OR R12
S7	2109	'CMOS MEMORY CIRCUITS' (January 1995)
S8	69388	'CMOS DIGITAL INTEGRATED CIRCUITS' OR CC='B2570D' OR CMOS - OR "C"MOS????? OR COMPLEMENTARY()(METAL????? OR MOS??????)
S9	10272	(S6:S7 OR MEMOR??? OR DATA()STOR????? OR DRAM? OR ROM OR R-OMS OR EPROM? OR EEPROM? ? OR SDRAM? OR SRAM?) AND S8
S10	6	(S6:S7 OR MEMOR??? OR DATA()STOR????? OR DRAM? OR ROM OR R-OMS OR EPROM? OR EEPROM? ? OR SDRAM? OR SRAM?) AND SUBSTRATE(-2N)TRANSISTORS
S11	2	S9 AND MONOCRYST?
S12	1	S9 AND MONO()CRYST?
S13	27	S9 AND SINGLE(1W)CRYST?
S14	36	S10:S13 NOT S5
S15	8	S14/2004-2006
S16	28	S14 NOT S15
S17	0	S16 AND MULTI?????
S18	0	S16 AND MULTI
S19	2	S16 AND MULTIPLE
S20	1	S16 AND LEVEL
S21	0	S16 AND LEVELS
S22	2	S19:S21
S23	486	S1:S22 AND (AREA OR AREAS)(4N)(COMPAR?????? OR DIFFER?????-??)
S24	2	S23 AND CENTER??????
S25	0	S23 AND CENTRE??????
S26	6	S23 AND PITCH?????
S27	16	S23 AND ASPECT?????
S28	6	S23 AND DISTANCE?
S29	82	S23 AND ARRAY?
S30	1	S23 AND MICROARRAY?
S31	179	S23 AND MU
S32	13	S23 AND MUM
S33	156	S23 AND MICRON? ?
S34	2	S23 AND S27 AND S29
S35	4	S23 AND S27 AND S31
S36	5	S23 AND S27 AND S33
S37	140	S23 AND S31 AND S33
S38	29	S23 AND S29 AND S33
S39	26	S23 AND S29 AND S31
S40	3	S5 OR S22
S41	55	S10:S12 OR S24:S26 OR S28 OR S30 OR S34:S36 OR S38
S42	55	S41 NOT S40
S43	6	S42 AND AREAS
S44	42	S42 AND AREA
S45	0	S42 AND RELATIV?????(4N)AREA??
S46	1	S42 AND RATIO(4N)AREA??
S47	0	S42 AND RATIOS(4N)AREA??
S48	3	S42 AND SMALLER(4N)AREA??

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S49          1    S42 AND LARGER(4N)AREA??
S50         10    S43 OR S46 OR S48 OR S49
S51         10    S50 NOT  (S22 OR S5)  .
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